



强 达 欣 多层电路(香港)有限公司
Q&D Multilayer PCB CO., Limited

Q&D Multilayer HDI Micro-vias manufacture Process & Technology Capability

DATE: November 08, 2022



Base material

General Material	Normal Tg:S1141, KB6160, KB6164, NP-140F, H140A
Lead-free Material	Mid Tg:VT-481, S1000H, H150, NP-155F High Tg: S1000-2M,IT180A, NP-175FB,EM827,370HR
Halogen Free Material	S1150G,S1170G,NPG-170N, NPG-151,EM-285
Low Loss DK&DF	NPG-170D,TU872LK
countersink	82° ,60° ,90° ,100° ,120° ,Other angles need to be customized
Solder Mask	TAIYO:PSR-2000,PSR-4000, SR-500 G40, SR-500 LB01HF, SR-500 HR30 RONG DA:H-9100, PSR-2000 CE800YE HONG TAI: HT-50 DG4; RONG DA: H-8100
Special Material	Cu-matrix、 Aluminium base、 Teflon PTFE、 Ceramic materials,Dupont
Surface Finish	Lead Free HASL、 ENIG、 OSP、 Immersion-Silver、 Immersion-Tin、 Gold Finger

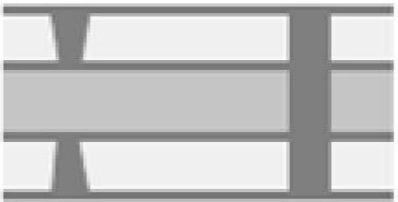
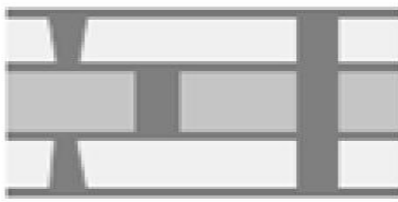
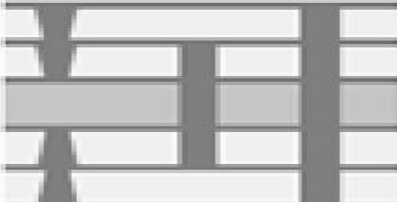


Base material

flexible base material (incline glue)	SF305: PI=0.5mil/1mil/2mil AD=13um/20um Cu=0.33oz/0.5oz/1oz
Flexible base material (exclue glue)	R-F775 PI=1mil/3mil Cu=0.33oz/0.5oz/1oz Dupont AP PI=1mil/2mil/3mil Cu=0.5oz/1oz ThinFlex(W/A): PI=1mil/2mil Cu=0.5oz/1oz
Coverlay	SF305(yellow):0515/0525/1025/2030 Taiflex(yellow):1025/1035
Adhesive	Taiflex: AD=10um/25um/4um SF315B:AD=13um/25um/40um
PI stiffener	Taiflex(MHK):PI=1mil/2mil/3mil/5mil/7mil/9mil
3M Tape	946/090/779/458/468
EMI	SF-PC600-U1 Protective layer:6um Glue:10um
No Flow PP	Ventec: VT-47NF EM-37B(L)



1.HDI boards types

类型	1+N+1			
压合结构				
	A 型	B 型	C 型	D 型
类型	2+N+2			
压合结构				
	A 型	B 型	C 型	D 型
类型	3+N+3			
压合结构				
	A 型	B 型	C 型	D 型



Process Capability & Technology Roadmap

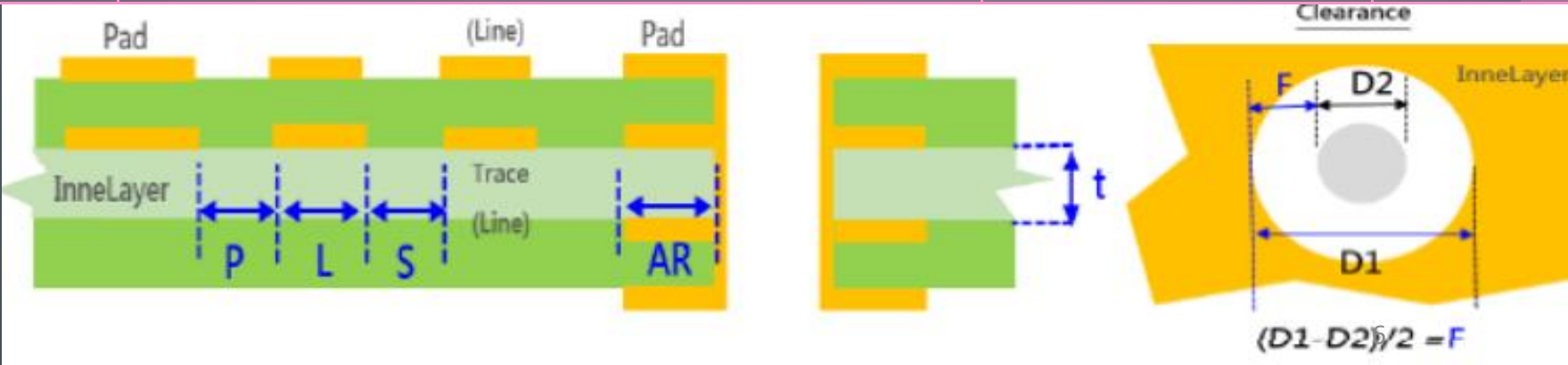
1. 可生产板尺寸与板厚 Available Working Panel Size & Board Thickness

项目 Capability Item	2022制程能力 Capability		
Layer	4	6	8-60
Finished Board Dimension (Max)	596*900mm	590*900mm	590*800mm
Finished Board Dimension (Min)	70*50mm	70*50mm	70*50mm
Finished Board Thickness (Max)	6.5mm	6.5mm	6.5mm
Finished Board Thickness (Min)	0.3mm	0.4mm	0.6mm
Min. Inner Layer Core Thickness (excluding)	0.05mm	0.05mm	0.05mm
Finished Board Thickness Tolerance(Board Thickness>0.8mm)	± 8%	± 8%	± 8%
Finished Board Thickness Tolerance(0.4mm≤Board Thickness≤0.8mm)	± 0.076mm	± 0.076mm	± 0.076mm
Board Warp & Twist	≤0.5%	≤0.5%	≤0.5%



2、内层线路Inner-layer Circuit Formation

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
内层 Inner-layer Circuit Formation	最小基板厚度(t)Min. CCL Thickness	4 mil	3 mil
	线宽/间距(L/S) Min. Line / Space	2.5/ 3 mil	2.5/ 3 mil
	线至Pad最小间距(P) Min Line to Pad Space	3.5 mil	3 mil
	孔环(AR)Hole Annual Ring	4 mil	3 mil
	孔隔离环(F) (Hole Clearance)	7 mil	5.5 mil





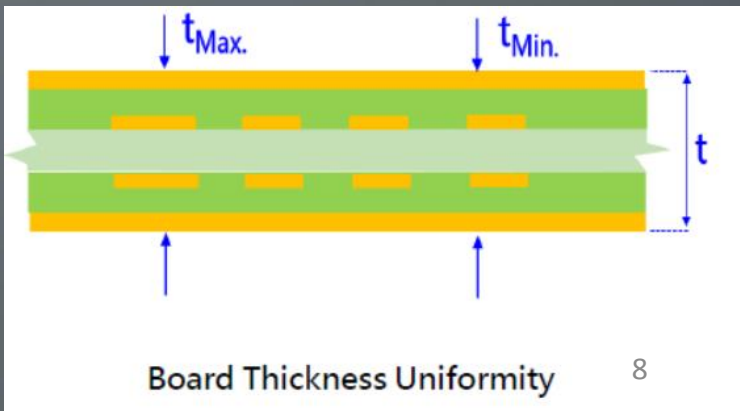
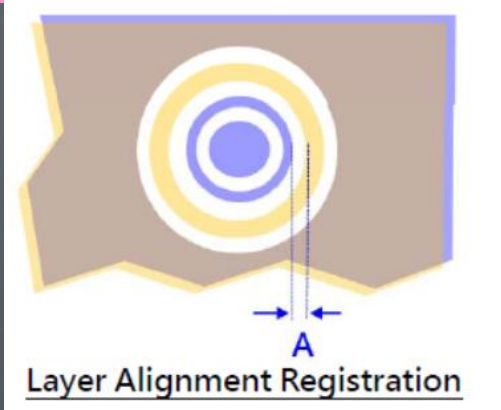
2、内层线路 Inner-layer Circuit Formation

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
内层线宽/线距 Inner-layer Line / Space)	H/HOZ	2.5mil/3mil	2.5mil/3mil
	1/1OZ	3.5mil/3.5mil	3mil/3mil
	2/2OZ	6mil/6mil	6mil/6mil
	3/3OZ	8mil/8mil	7.5mil/7.5mil
	4/4OZ	10mil/10mil	9mil/9mil
	6/6OZ	14mil/14mil	12mil/12mil



3、压合Lamination

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
压合 Lamination	层间偏移(A) Layer Alignment Registration	$\leq 2.5\text{mil}$	$\leq 2\text{ mil}$
	压合板厚均匀性 Board Thickness Uniformity tmax-tmin	$\leq 4\text{ mil}$	$\leq 3\text{ mil}$
	压合板厚公差 Board Thickness Tolerance	$\pm 10\%$	$\pm 8\%$
	可使用铜箔厚度 Copper Foil	1/3~6oz	1/3~12oz





4、钻孔&电镀铜 Drilling & Cu Plating

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
钻孔 Drilling	最小孔径 (Min. Hole Dimension of Drilling)	6 mil	6 mil
	PTH孔径公差 PTH Hole Dimension Tole	± 3mil	± 2mil
	NPTH孔径公差 NPTH Hole Dimension Tole	± 2mil	± 2mil
	孔间距 hole distance	12mil	10mil
	孔位精度 Hole Position Accuracy of Drilling	≤ 3 mil	≤ 2 mil
Laser microvia hole and blind hole	Mechanical blind via	aspect ratio 0.5:1	aspect ratio 0.5:1
	Laser Microvias	aspect ratio 0.8:1	aspect ratio 0.8:1
	Mininum laser hole size	3mil	3mil



Process Capability & Technology Roadmap

4、钻孔&电镀铜 Drilling & Cu Plating

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
Laser microvia hole and blind hole	Max laser hole size	6 mil	6 mil
	PTH孔径公差 PTH Hole Dimension Tole	$\pm 3\text{mil}$	$\pm 2\text{mil}$
	NPTH孔径公差 NPTH Hole Dimension Tole	$\pm 2\text{mil}$	$\pm 2\text{mil}$
	孔间距 hole distance	12mil	10mil
	孔位精度 Hole Position Accuracy of Drilling	$\leq 3\text{ mil}$	$\leq 2\text{ mil}$
	Laser method	1)DLD 2) Etch copper for large window before laser driling	1)DLD 2) Etch copper for large window before laser drilling
电镀铜 Cu Plating	纵横比 Aspec Ratio	10:1	12:1
	电镀铜厚均匀性 Plating Uniformity	$R \leq 0.4\text{mil}$	$R \leq 0.3\text{mil}$
	可电镀铜最小孔径 Mini. Hole Dimension of Plating	6 mil	6 mil



5、外层线路 Outer-layer Circuit Formation

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
外层 Outer-layer Circuit Formation	最小线宽 / 间距 (L/S) Line / Space	3/3 mil	2.5/3 mil
	线至Pad间距 Line to Pad Space	$\geq 4\text{mil}$	$\geq 3.5\text{ mil}$
	pad至Pad间距 Pad to Pad Space	$\geq 4\text{mil}$	$\geq 3.5\text{ mil}$
	外层孔环 external Annular ring	3mil	0 mil
	线宽公差 Line Width Tolerance (Normal)	$\pm 20\%$	$\pm 10\%$
	阻抗线宽公差 Line Width Tolerance (Impedance)	$\pm 8\%$	$\pm 8\%$



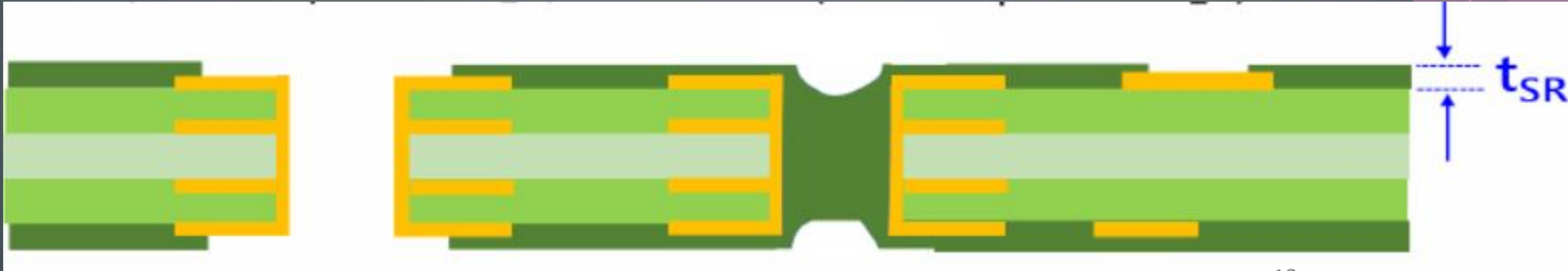
5、外层线路Outer-layer Circuit Formation

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
外层线宽/线 距 Outer-layer Line / Space)	1/10Z	3mil/3mil	2.5mil/3mil
	2/20Z	7.8mil/7.8mil	6mil/6mil
	3/30Z	8.5mil/8.5mil	8mil/8mil
	4/40Z	12mil/12mil	10mil/10mil
	6/60Z	14mil/14mil	12mil/12mil



6、防焊Solder Resistance

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
防焊 Solder Resistance	防焊基材面油墨厚度 SR Thickness t	$\geq 0.4\text{mil}$	$\geq 0.4\text{mil}$
	防焊塞孔能力 SR Plug-in	8-20mil	6-20mil
	通孔显影能力 Developping Capability for Hole	13 .8mil	11. 8mil
	防焊开口至线路距离 (D)SR Opening to Line (Pad) Min.Distance	2mil	1.5mil
	防焊开窗Ring	1.5mil	1.0mil





6、防焊Solder Resistance

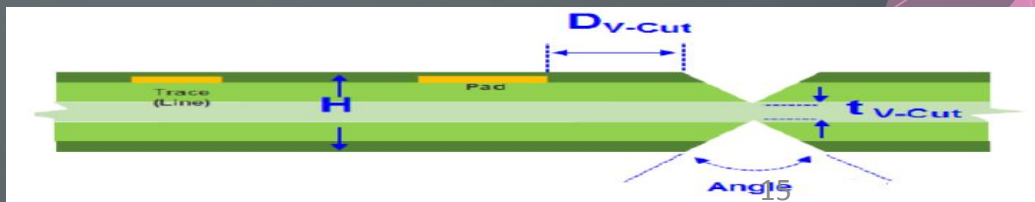
制程 Process	项目 Capability Item		2021制程能力 Capability	2022制程能力 Capability
防焊 Solder Resistance	防焊隔焊最小宽度 (Dam)Min. Solder Dam Dsgn (by SR Type)	白 / 黑	4mil	3.5mil
		绿 / 蓝 / 红 / 黄	3.5mil	3mil
	文字线宽 (L) Min. Legend Width		$\geq 5 \text{ mil}$	$\geq 4 \text{ mil}$
	文字至PAD最小距离 (P)Min. Space of Legend to PAD		$\geq 8 \text{ mil}$	$\geq 5 \text{ mil}$





7、成型Routing

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
成型Routing	成型尺寸公差 Dimension Tolerance	$\pm 4\text{mil}$	$\pm 3\text{mil}$
	Pad至板边最小距离 Min. Pad to Edge Distance	8mil	7mil
	Pad至板边公差 Pad to Edge Tolerance	$\pm 6\text{mil}$	$\pm 5\text{mil}$
	V-cut残厚 Remain thickness of V-cut	8mil (min)	8mil (min)
	V-CUT角度 V-CUT Angle Tolerance ($30^\circ \sim 60^\circ$)	$\pm 5^\circ$	$\pm 5^\circ$
	Pad至V-Cut边最小距离(DV-Cut) Min. Pad to V-Cut Distance	$\geq 16\text{ mil}$	$\geq 16\text{ mil}$
	Control depth Drill	accuracy	$\pm 75\mu\text{m}$





Process Capability & Technology Roadmap

8、表面处理及其它 Metal Finish & Others

制程 Process	项目 Capability Item	2021制程能力 Capability	2022制程能力 Capability
化学镍金 ENIG	镍厚 Ni Thickness	3~6um	3~6um
	金厚 Au Thickness	0.03~0.1um	0.03~0.1um
OSP	膜厚 OSP film thickness	0.2~0.5um	0.2~0.5um
化锡 Immersion Tin	锡厚 Immersion Tin Thickness	$\geq 1\mu\text{m}$	$\geq 1\mu\text{m}$
化银 Immersion Silver	银厚 Immersion Silver Thickness	0.15~0.4um	0.15~0.4um
无铅喷锡 Lead-free HASL	锡厚 Tin Thickness	1~40um	1~40um
碳油 Carbon oil	碳油 Carbon oil Impedance	10~25 Ω /sqcm	10~25 Ω /sqcm
其它 Others	板弯翘 Board Warp & Twist	0.5%	0.5%
	FD-FD公差 Fidical Mark To Fidical Mark	$\pm 3\text{mil}$	$\pm 2\text{mil}$
	阻抗公差 Impedance Tolerance	$\pm 10\%$	$\pm 8\%$



Process Capability & Technology Roadmap

Item	2021	2022
FR-4+Ceramic Hybrid Rogers	Yes	Yes
Heavy copper PCB	Yes	Yes
Gold Finger	Yes	Yes
Via in Pad (POFV)	Yes	Yes
Selective Gold Plating	Yes	Yes
Embedded Capacitor PCB	Yes	Yes
Flexible PCB	Yes	Yes
Rigid-Flex PCB	Yes	Yes



Process Capability & Technology Roadmap

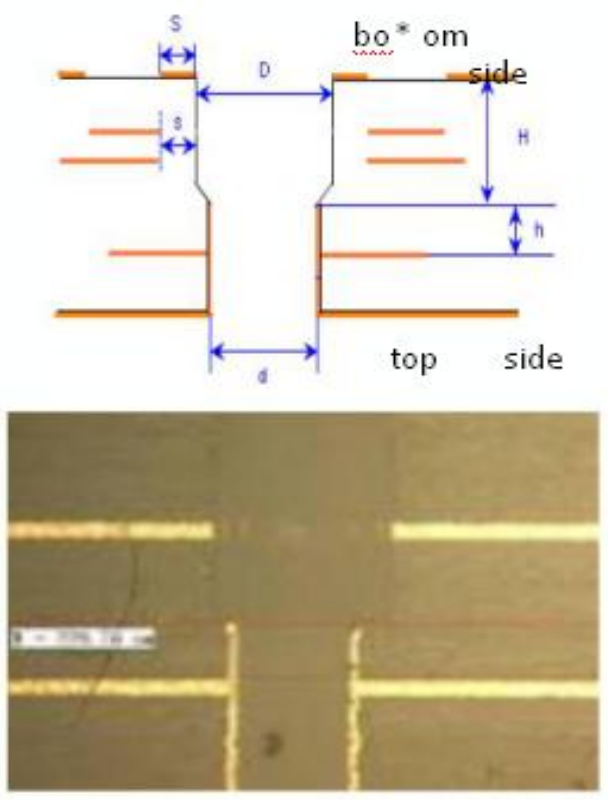
Item	2021	2022
Blind Hole Board	Yes	Yes
Buried Hole Board	Yes	Yes
HDI (1+N+1) (2+N+2) (3+N+3)	Yes	Yes
HDI (4+N+4)	Yes	Yes
HDI (5+N+5)	Yes	Yes
High-frequency and high-speed board	Yes	Yes



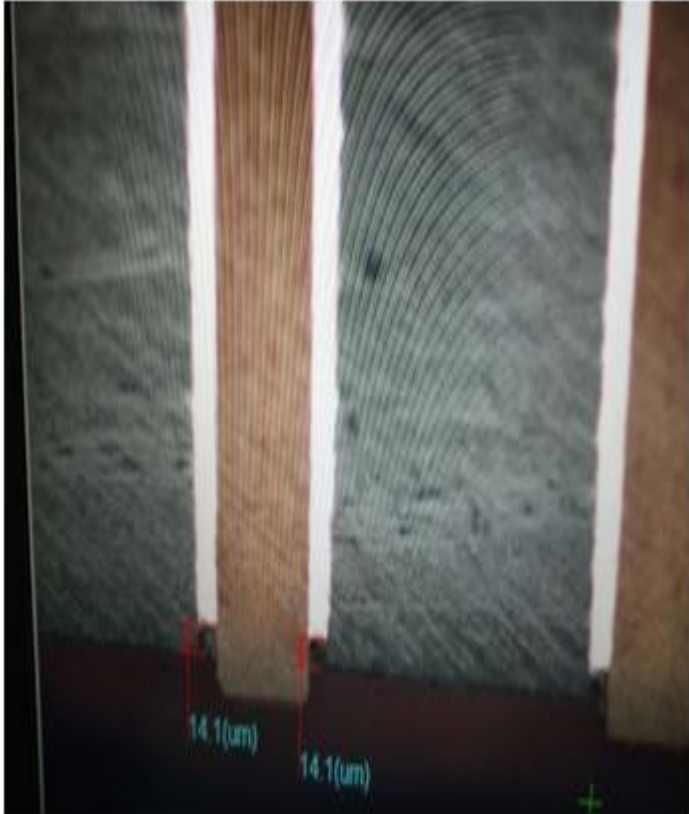
Process Capability & Technology Roadmap

Item	2021	2022
Layers (Max)	48L	60L
Board Thickness (Max)	6.5mm	6.5mm
Board Thickness (Min)	0.3mm	0.3mm
Core Thickness (Min) (excluding Cu Thickness)	0.076mm	0.05mm
PP Thickness (Min)	0.076mm	0.05mm

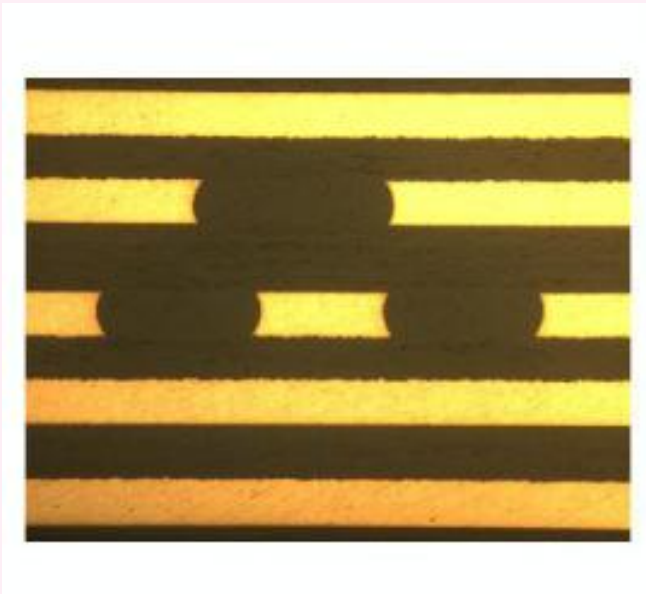


Back Drilling		NP diameter(D)	$\geq 0.45 \text{ mm}$
		PTH diameter (d)	$\geq 0.15 \text{ mm}$
		$(D - d) \div 2$	$\geq 0.10\text{mm}$
		Min. Clearance (S)	0.175 mm
		Min Stub	0.20mm

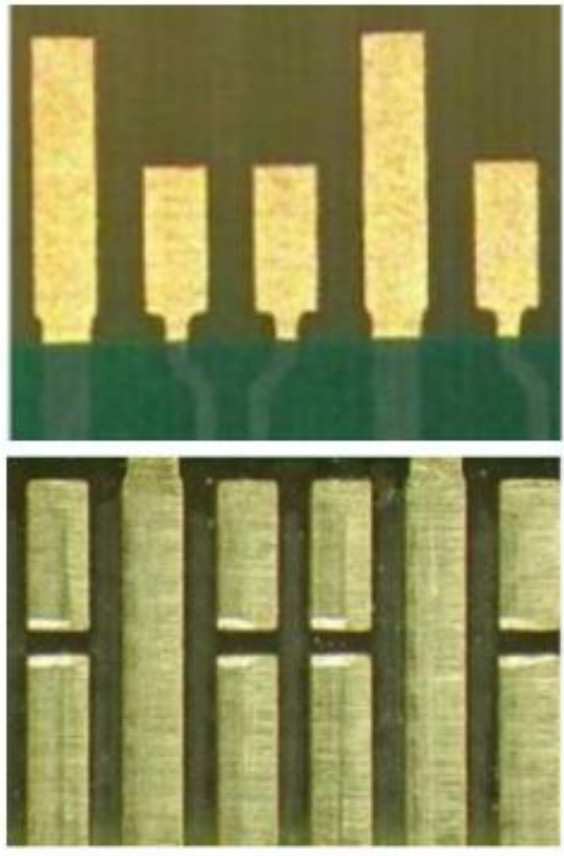


Back Drilling		Etching back drilling	Depth $\geq 10\mu\text{m}$

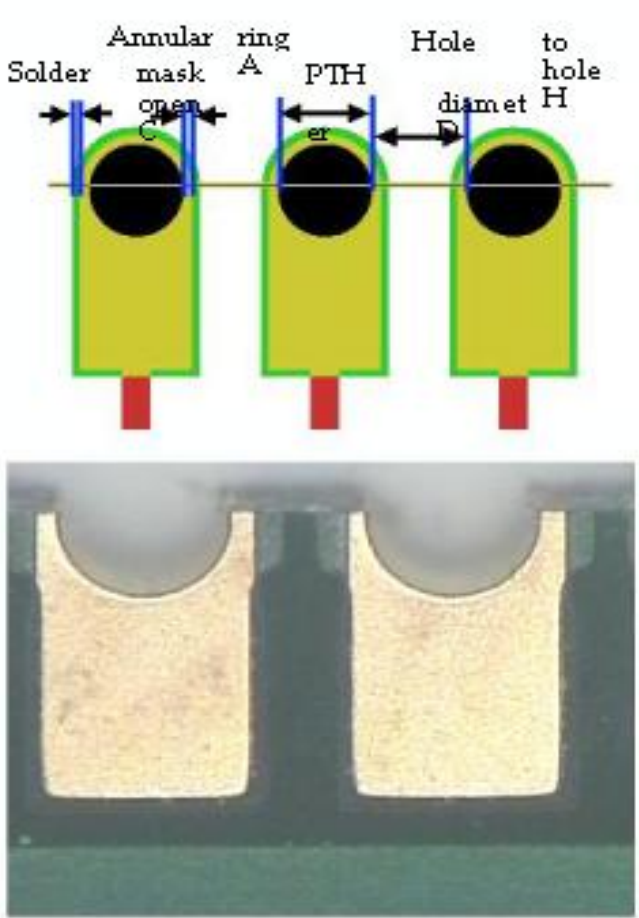


Heavy Copper PCB		Max. inner layer copper	60Z
		Min. inner layer line width	12mil
		Min. inner layer annular ring	12mil
		Max. out layer copper	120Z
		Min. out layer line width	10mil
		Min. out layer annular ring	1mil
		Min. Clearance	11mil
		Min. dielectric thickness	22 5.3mil

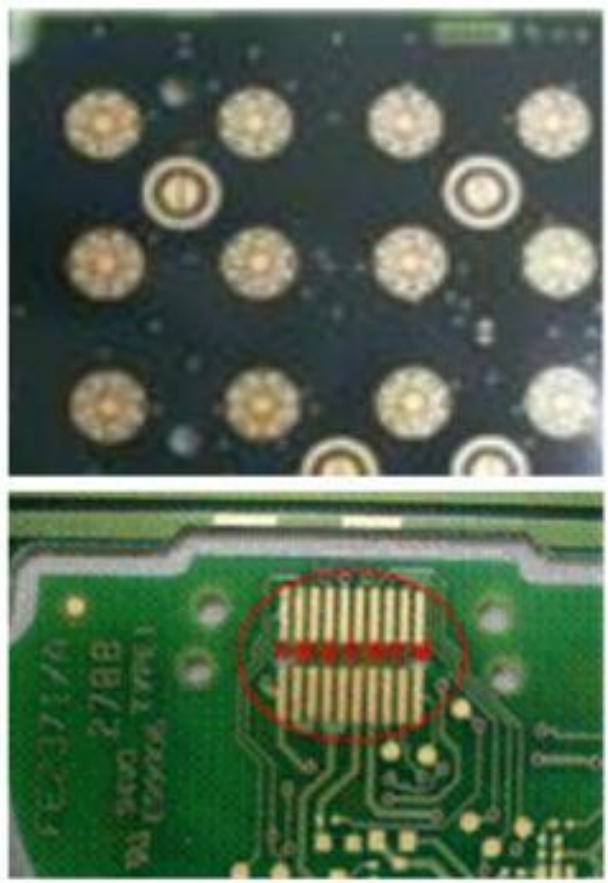


Glod Finger with Different Length		Min. pcb width of finger tolerance	$\pm 75\mu\text{m}$
		Min. finger width tolerance	$\pm 0.025\text{mm}$
		finger length tolerance	0 - 0.15mm
		finger edge to board edge length tolerance	$\pm 0.1\text{mm}$



PTH Half-hole		Min. PTH diameter (D)	0.3mm
		Min. Hole edge to hole edge (H)	0.3mm
		Min. Annular ring (A)	0.15mm
		Min. Solder mask open (C)	0.05mm

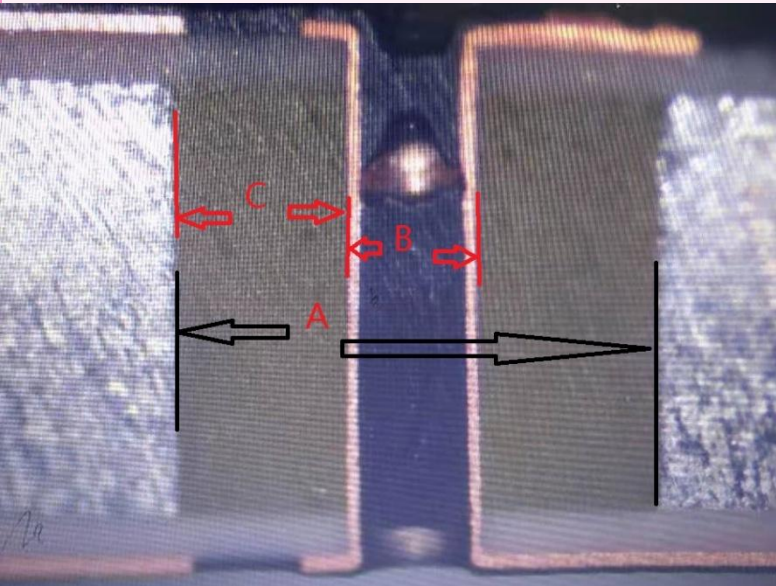


Selective Gold Plating		Max. nickle thickness	6μm
		Max. gold thickness	2.0μm



Resin Plug-hole		Aspect ratio of Resin plugging hole	10:1
		Dimple	≤25um



Aluminum Substrate Drilling		Buried hole	$A-B \geq 0.6\text{mm}$
		Through hole	$B \geq 0.2\text{mm}$
		Distance from through hole to aluminum plate	$C \geq 0.3\text{mm}$



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You are welcome to contact
QD Multilayer any time ! Thanks



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